

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Accompanying Continuation Application
Under 37 CFR 1.53(b) :

Prior Application: K. ISHIBASHI et al
Serial No. 10/300,747
Filed: November 21, 2002

Group Art: 2815
Examiner: E. Wojciechowicz
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Alexandria, VA 22314

Sir:

In accordance with the duty of disclosure, the Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 10/300,747.

The above-referenced patent application is a continuation application of U.S. Serial No. 10/300,747, filed November 21, 2002, which is a continuation application of U.S. Serial No. 10/109,669, filed April 1, 2002, now U.S. Patent No. 6,563,180, which is a continuation application of U.S. Serial No. 09/987,820, filed on November 21, 2001, now U.S. Patent No. 6,424,015), which is a continuation of US Serial No. 09/604,513, filed June 27, 2000 (abandoned), from which priority is claimed under 35 USC §120.

The Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,

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Date: December 10, 2003

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. NIT-204-05	SERIAL NO.		
LIST OF DOCUMENTS CITED BY APPLICANT <i>(Use several sheets if necessary)</i>				APPLICANT K. ISHIBASHI et al			
				FILING DATE December 10, 2003	GROUP		
U.S. PATENT DOCUMENTS							
* EXAMINER INITIAL		DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE <i>(If Appropriate)</i>
	AA	6,215,159	04/2001	Fujita et al			
	AB	5,212,663	05/1993	Leong			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
	AL	10-65517	03/06/98	Japan			<input type="checkbox"/> <input type="checkbox"/>
	AM	10-242292	09/11/98	Japan			<input type="checkbox"/> <input type="checkbox"/>
	AN	2000-277627	10/06/00	Japan			<input type="checkbox"/> <input type="checkbox"/>
	AO						<input type="checkbox"/> <input type="checkbox"/>
	AP						<input type="checkbox"/> <input type="checkbox"/>
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, etc.)</i>							
	AR	Hiroki MORIMURA and Nobutaro SHIBATA, "A 1-V 1-Mb SRAM for Portable Equipment", 1996, pp 61-66, Kanagawa-ken, JAPAN.					
	AS						
	AT						
EXAMINER				DATE CONSIDERED			